

NAK1-AN73

PATENT APPLICATION



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Masato Suzuki et al.

Serial No.: 08/249,157

Filed: May 26, 1994

For: PROGRAM CONVERTING UNIT
AND PROCESSOR IMPROVED
IN ADDRESS MANAGEMENT

Examiner: S. Kang

Group Art Unit: 2315

December 1, 1994

Irvine, CA 92714

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GROUP 2315

INFORMATION DISCLOSURE STATEMENT

Commissioner of
Patents and Trademarks
Washington, DC, 20231

Dear Sir:

In an attempt to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§1.97 and 1.98, applicants wish to bring to the attention of the U.S. Patent Office the following references which were disclosed during an official search for a co-pending European application:

"Address Size Independence in a 16-Bit Minicomputer", by Philip E. Stanley, The 5th Annual Symposium on Computer Architecture, April 1978

"High-Performance Single-Chip Microcontroller H8/300 Series", by Nobuo Shibasaki et al., Hitachi Review, Vol. 40 (1991), No. 1

European Patent Application No. 0 503 514 A2

European Patent Application No. 0 148 478 A2

U.S. Patent No. 4,314,332

Copies of these references and a form PTO 1449 are enclosed.

The undersigned attorney hereby certifies that each item contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application no more than three months prior to the filing of this statement.

If the Examiner believes that a telephone interview would help further the prosecution of this case, he is respectfully requested to contact the undersigned attorney at the listed telephone number.

Respectfully submitted,


PRICE, GESS & UBELL

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the commissioner of Patents and Trademarks, Washington DC 20231

on December 2, 1994
Eric Hoover


Signature

December 2, 1994
Date


Joseph W. Price
Reg. No. 25,124
Attorneys for Applicant
2100 S. E. Main St., Suite 250
Irvine, CA, 92714
(714) 261-8433